OBSERVATION:

      Observation Table:

Inputs		Output	Remarks
A	B	Y	LED is ON/OFF
0	0		
0	1		
1	0		
1	1		

RESULT:

      The truth table shows that for the AND gate, the output is low only if both the inputs are low.

CONCLUSION:

      The logic operation of AND gate has been designed and its truth table has been verified.

SOURCES OF ERROR:

1. Error may be due to the carelessness of instrument.
2. The connection may not be tight.
3. The supplied diode may be faulty.
4. Error may be due to the resistance of connecting wire.

PRECAUTION:

1. The circuit should be properly connected.
2. The current in the circuit should not be suddenly increased.
3. Suitable voltage and current should be applied.
4. Connected wire should be uniform, low resistivity like copper, etc.
5. Reading should be noted carefully.

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